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7590

01/22/2004

Miles & Stockbridge P.C.
 Suite 500
 1751 Pinnacle Drive
 McLean, VA 22102-3833



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|--------------------|
| (Depositor's name) |
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/083,402 | 02/27/2002 | Yutaka Shimada | XA-9629 | 7383 |

TITLE OF INVENTION: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 04/22/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|------------------|----------|----------------|
| EL ARINI, ZEINAB | 1746 | 134-006000 |

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

Mitchell W. Shapiro (Date) 3/31/04
 Mitchell W. Shapiro Reg. No. 31,568

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